

PIPO COMMUNICATIONS		
Title	PK-7/8K V6 (4/2/2021 9:31 AM)	
Size A	Document Number	Rev A
Date: Wednesday, March 31, 2021	Sheet 1 of 1	

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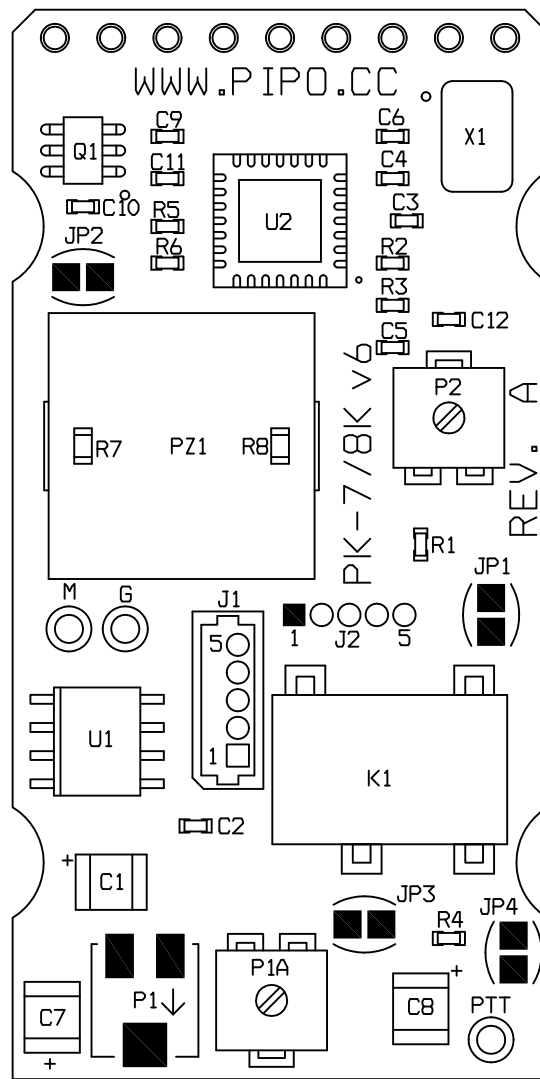
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REVISIONS		
DESCRIPTION	DATE	APPROVED



- 5. J2, 9 HOLES AT TOP OF BOARD, "PTT", "M" AND "G" HOLES ARE TO BE VOID OF SOLDER.
- 4. R7 AND R8 ARE USED AS SPACERS TO INSTALL PZ1. PZ1 IS INSTALLED BY HAND. IT IS NOT SEALED.
- 3. DO NOT INSTALL THE FOLLOWING COMPONENTS: J2, JP1-JP4.

- 2. INSTALL COMPONENTS, MAKE ALL INTERFACIAL CONNECTIONS, ASSEMBLE AND SOLDER IAW J-STD-001, CLASS 2.
- 1. SEE SEPARATE PARTS LIST FOR COMPONENT VALUE AND/OR USAGE.

NOTES: UNLESS OTHERWISE SPECIFIED.

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN INCHES TOLERANCES ARE: <u>DECIMALS</u> <u>ANGLES</u> .X = ± .1 .XX = ± .03 ± 0°30' .XXX = ± .005 DO NOT SCALE DRAWING	CONTRACT NO.		PIPO COMMUNICATIONS HOLLYWOOD, CALIFORNIA			
	APPROVALS				DATE	
	DWN. R. GONZALES		6/13/2018	TITLE		
	CHK.			PCB ASSEMBLY:		
	ENG.			PK-7/8K v6		
APP.						
TREATMENT	THIS DRAWING PRODUCED BY:		SIZE	CAGE CODE	DWG NO.	REV
FINISH	P.C.D.C.		B			A
CAD FILE PK-7-8K v6-ASSY			SCALE 3 : 1		SHEET 1 OF 1	

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